

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	10/317229	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/31 12:17
L2	1	10/317229 and representation	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/31 13:11
L3	3430	700/108\$.ccls. or 707/109\$.ccls. or 707/110\$.ccls. or 702/179\$. ccls. or 702/182\$.ccls. or 702/188\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/31 13:12
L4	7	3 and (observ\$5 same indicat\$4 same defect\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/31 13:23
L5	265	3 and (defects)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/31 13:23
L6	32	3 and (defects same database)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/31 13:55
L7	39	3 and (defects and database and repair)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/31 13:55
L8	17490	707/3 or 707/10 or 707/102	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/31 13:56
L9	20834	8 or 3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/31 13:56

L10	110948	9 nd (quality same database same track\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/31 13:56
L11	8436	10 and defects	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/31 14:00
L12	634	10 and defects and (manufacturing same defects)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/31 14:00
L13	41	12 and (defect same (frequency or statistic))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/31 14:01

File 348:EUROPEAN PATENTS 1978-2005/Aug W03

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File 349:PCT FULLTEXT 1979-2005/UB=20050825,UT=20050818

(c) 2005 WIPO/Univentio

File 324:German Patents Fulltext 1967-200534

(c) 2005 Univention

Set	Items	Description
S1	1700276	TOKEN? ? OR PRODRAM? OR PRECURS? OR OMEN? ? OR INDICANT? ? OR INDICAT???? ?
S2	1928139	EARMARK? OR EAR()MARK? ? OR TRAIT? ? OR CHARACTERISTIC? ? - OR HINT??? ? OR CLUE? ? OR MANIFESTATION? ?
S3	557952	SYMPTOM? OR SIGN? ?
S4	1046071	SIGNAL? ?
S5	149523	S1:S4(5N) (CATEGORY? OR CATEGORIES OR CLASSIFY? OR CLASSE? ? OR CLASSIFIE? OR CLASSIFICATION? OR GROUP???? ? OR CATALOG?)
S6	1	S1:S4(5N) (AUTOCATEGOR? OR AUTOCLASS? OR AUTOGROUP? OR AUTO- CATALOG?)
S7	450	S1:S4(5N)TAXONOM?
S8	140595	S1:S4(5N) (DEFECT???? ? OR MISTAKE? ? OR FAIL???? ? OR PROB- LEM???? ? OR FAULT?? ? OR DEFICIEN? OR ABNORMA? OR DAMAGE? ?)
S9	23728	S1:S4(5N) (MALFORM? OR ATYPIC? OR FLAW?? ? OR IMPAIR? OR IM- PERFECT? OR NONUNIFORM? OR BLEMISH? OR ABERRA? OR NONCONFORM?)
S10	118495	S1:S4(5N) (MALFUNCTION? OR NONSTANDARD? OR INOPERA? OR DYSF- UNCT? OR DISFUNCT? OR BUG? ? OR BUGGY OR ERROR? ? OR DEVIA?)
S11	73651	S1:S4(5N) (IRREGULAR? OR CORRUPT? OR IMBALANC? OR EXCEPTION? ? OR DISTORT? OR DISCREPAN? OR DEGRAD? OR DISPARAT? OR DIVER- G?)
S12	31618	S1:S4(5N)EVENT? ?
S13	2487	S1:S4(5N)NON() (UNIFORM? OR CONFORM? OR STANDARD?)
S14	971208	FREQUEN? OR RECUR? OR REOCCUR?
S15	1713353	OCCUR? OR INCIDEN? OR OCCASION? ? OR INSTANCE?
S16	35516	(COUNT??? ? OR QUANTITY OR NUMBER OR QUANTIF? OR COMPIL? OR TABULAT? OR CALCULAT? OR COMPUT??? ? OR COMPUTAT?) (3W)S15
S17	2286	(CAPTUR? OR ASCERTAIN?) (3W)S15
S18	3810	S5:S7(20N)S8:S13
S19	298	S18(20N)S14
S20	9	S18(20N)S16:S17
S21	13086	IC='G06F-011'
S22	23540	IC='G06F-015'
S23	17560	IC='G06F-017/30'
S24	10	S19 AND S21:S23
S25	10	S24 NOT S20
S26	142	S18 AND S21
S27	4	S26 AND S23
S28	63970	S14(5N)S15
S29	8	S26 AND S28
S30	137807	S14:S15(3N) (DEFECT???? ? OR MISTAKE? ? OR FAIL???? ? OR PR- OBLEM???? ? OR FAULT?? ? OR DEFICIEN? OR ABNORMA? OR DAMAGE? - ?)
S31	8963	S14:S15(3N) (MALFORM? OR ATYPIC? OR FLAW?? ? OR IMPAIR? OR - IMPERFECT? OR NONUNIFORM? OR BLEMISH? OR ABERRA? OR NONCONFOR- M?)
S32	62342	S14:S15(3N) (MALFUNCTION? OR NONSTANDARD? OR INOPERA? OR DY- SFUNCTION? OR DISFUNCTION? OR BUG? ? OR BUGGY OR ERROR? ? OR - DEVIA?)
S33	35170	S14:S15(3N) (IRREGULAR? OR CORRUPT? OR IMBALANC? OR EXCEPTI- ON? ? OR DISTORT? OR DISCREPAN? OR DEGRAD? OR DISPARAT? OR DI- VERG?)
S34	40366	S14:S15(3N)EVENT? ?
S35	1721	S14:S15(3N)NON() (UNIFORM? OR CONFORM? OR STANDARD?)

S36 255 S18(20N)S30:S35
S37 20 S36 AND S21:S23
S38 17 S37 NOT (S20 OR S25 OR S27 OR S29)
S39 17 IDPAT (sorted in duplicate/non-duplicate order)
S40 17 IDPAT (primary/non-duplicate records only)
S41 9 S40 AND AC=US/PR
S42 6 S41 AND AY=(1970:2000)/PR
S43 10 S40 AND PY=1970:2000
S44 11 S42:S43
S45 67 S36/TI,AB,CM
S46 61 S45 NOT (S20 OR S25 OR S27 OR S29 OR S37)
S47 61 IDPAT (sorted in duplicate/non-duplicate order)
S48 60 IDPAT (primary/non-duplicate records only)
S49 34 S48 AND AC=US/PR
S50 23 S49 AND AY=(1970:2000)/PR
S51 39 S48 AND PY=1970:2000
S52 42 S50:S51
S53 79272 PRODROM? OR INDICANT? ? OR SYMPTOM?
S54 2242 S53(5N) (CATEGORY? OR CATEGORIES OR CLASSIFY? OR CLASSE? ? -
OR CLASSIFIE? OR CLASSIFICATION? OR GROUP???? ? OR CATALOG?)
S55 0 S53(5N) (AUTOCATEGOR? OR AUTOCLASS? OR AUTOGROUP? OR AUTOCA-
TALOG?)
S56 3 S53(5N)TAXONOM?
S57 1666 S53(5N) (MALFORM? OR ATYPIC? OR FLAW?? ? OR IMPAIR? OR IMPE-
RFECT? OR NONUNIFORM? OR BLEMISH? OR ABERRA? OR NONCONFORM?)
S58 2564 S53(5N) (MALFUNCTION? OR NONSTANDARD? OR INOPERA? OR DYSFUN-
CT? OR DISFUNCT? OR BUG? ? OR BUGGY OR ERROR? ? OR DEVIA?)
S59 5489 S53(5N) (DEFECT???? ? OR MISTAKE? ? OR FAIL???? ? OR PROBLE-
M???? ? OR FAULT?? ? OR DEFICIEN? OR ABNORMA? OR DAMAGE? ?)
S60 539 S53(5N) (IRREGULAR? OR CORRUPT? OR IMBALANC? OR EXCEPTION? ?
OR DISTORT? OR DISCREPAN? OR DEGRAD? OR DISPARAT? OR DIVERG?)
S61 546 S53(5N)EVENT? ?
S62 7 S53(5N)NON() (UNIFORM? OR CONFORM? OR STANDARD?)
S63 162 S54:S56(20N)S57:S62
S64 13 S63(20N)S30:S35
S65 12 S64 NOT (S20 OR S25 OR S27 OR S29 OR S37 OR S52)
S66 13 S63 AND S21:S23
S67 0 S66 NOT (S20 OR S25 OR S27 OR S29 OR S37 OR S52 OR S66)
S68 22 S63/TI,AB,CM
S69 15 S68 NOT (S20 OR S25 OR S27 OR S29 O

? t27/5,k/1,3-4

27/5,K/1 (Item 1 from file: 348)

DIALOG(R) File 348:EUROPEAN PATENTS

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01936456

Automatic invocation of computational resources without user intervention

Automatisches Aufrufen von Rechnermitteln ohne Bedienereingriff

Appel automatique de ressources logicielles sans intervention de l'utilisateur

PATENT ASSIGNEE:

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PATENT (CC, No, Kind, Date): EP 1560131 A2 050803 (Basic)

APPLICATION (CC, No, Date): EP 2005009944 931125;

PRIORITY (CC, No, Date): US 34458 930319

DESIGNATED STATES: DE; FR; GB; NL

RELATED PARENT NUMBER(S) - PN (AN):

EP 616288 (EP 93119006)

INTERNATIONAL PATENT CLASS: **G06F-017/30 ; G06F-009/44; G06F-011/22**

ABSTRACT EP 1560131 A2

A system is described for automatically invoking computational resources without intervention or request from a user of the system. In the system a query-free information retrieval system is described in which the exact technical documentation contained in existing user or other technical manuals is provided to a user investigating apparatus having a fault. The user enters symptoms based upon the user's analysis of the apparatus, and in response the system provides information concerning likely faults with the apparatus. As the symptoms are entered, the relative value of individual faults is determined and related to the symptoms they cause. The user can then select technical information relating to probable faults in the system.

ABSTRACT WORD COUNT: 113

NOTE:

Figure number on first page: 1

LEGAL STATUS (Type, Pub Date, Kind, Text):

Application: 050803 A2 Published application without search report

Examination: 050803 A2 Date of request for examination: 20050506

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	200531	741
SPEC A	(English)	200531	6209
Total word count - document A			6950
Total word count - document B			0
Total word count - documents A + B			6950

INTERNATIONAL PATENT CLASS: **G06F-017/30 ...**

... G06F-011/22

...SPECIFICATION a fault has been influenced (either positively or

negatively) by the new symptom, then the **symptom** becomes a member of the **fault** 's support **group** . After each new **symptom** instantiation, all modified support **groups** are evaluated, producing a new set of primary topics for the user to view in...

27/5,K/3 (Item 3 from file: 348)

DIALOG(R) File 348:EUROPEAN PATENTS

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00428030

A method for executing computer-assisted physical fault diagnosis by means of an expert system

Verfahren zur Durchführung einer rechnergestützten Diagnose von physikalischen Fehlern mittels eines Expertensystems

Methode pour mettre en oeuvre un diagnostic assiste par ordinateur de défauts physiques au moyen d'un systeme expert

PATENT ASSIGNEE:

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INVENTOR:

Thomason, Graham Gordon, c/o Int. OCTROOIBUREAU B.V., Prof. Holstlaan 6, NL-5656 AA Eindhoven, (NL)

LEGAL REPRESENTATIVE:

Peters, Rudolf Johannes et al (49051), INTERNATIONAAL OCTROOIBUREAU B.V., Prof. Holstlaan 6, 5656 AA Eindhoven, (NL)

PATENT (CC, No, Kind, Date): EP 443212 A1 910828 (Basic)
EP 443212 B1 970611

APPLICATION (CC, No, Date): EP 90200420 900222;

PRIORITY (CC, No, Date): EP 90200420 900222

DESIGNATED STATES: DE; FR; GB

INTERNATIONAL PATENT CLASS: G06F-009/44; **G06F-017/30** ; **G06F-011/22**

CITED PATENTS (EP A): US 4866635 A; US 4866635 A; EP 334113 A; US 4649515 A

CITED REFERENCES (EP A):

IEEE COMMUNICATIONS MAGAZINE, vol. 26, no. 3, March 1988, pages 6-13, New York, US; T.E. MARQUES: "A symptom-driven expert system for isolating and correcting network faults"

COMPUTER, vol. 19, no. 7, July 1986, pages 68-76, New York, US; F.

PIPITONE: "The FIS electronics troubleshooting system"

PROC. FIRST CONF. ON ARTIFICIAL INTELLIGENCE APPLICATION, 1984, pages 133-138; F. PIPITONE: "An expert system for electronics troubleshooting based on function and connectivity"

THE COMPUTER JOURNAL, vol. 28, no. 4, August 1985, pages 366-371, London, GB; M. SCHNEIDER: "Weighted decision tables - an alternative solution for ambiguity";

ABSTRACT EP 443212 A1

There is described a machine (20) assisted physical fault diagnosis method for a physical fault object. First a number (one or more) of physical fault(70)systems are collected. These link forward to physical fault syndromes(72) and these again to physical fault hypotheses (74). The links are automatically exploited in forward direction to generate a hypothesis ranking. Recurrently, starting from an actually preferred hypothesis, backward chaining to collection of uncollected physical fault symptoms and/or to associated fault attribute information and after this in the absence of a hypothesis prioritization change, backward chaining on a hypothesis goal, for thereby validating or, alternatively refuting the actually preferred hypothesis (76). After possible prioritization change, the process proceeds with the new preferred hypothesis, without necessarily excluding the demoted hypothesis from future consideration.

After possible refutation, the process proceeds with a new preferential hypothesis, having excluded the refuted hypothesis from subsequent consideration. The process continues until either a hypothesis is validated or the set of hypotheses related to at least a part of the actual symptom and symptom attribute information, is exhausted. (see image in original document)

ABSTRACT WORD COUNT: 182

LEGAL STATUS (Type, Pub Date, Kind, Text):

Application: 910828 A1 Published application (A1with Search Report
;A2without Search Report)
Change: 911009 A1 Designated Contracting States (change)
Examination: 920429 A1 Date of filing of request for examination:
920228
Examination: 951213 A1 Date of despatch of first examination report:
951025
Grant: 970611 B1 Granted patent
Change: 970910 B1 Representative (change)
Oppn None: 980603 B1 No opposition filed

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	EPABF1	900
SPEC A	(English)	EPABF1	6090
Total word count - document A			6990
Total word count - document B			0
Total word count - documents A + B			6990

...INTERNATIONAL PATENT CLASS: G06F-017/30 ...

... G06F-011/22

...SPECIFICATION and simplifies the use of the inventive method.

Advantageously, items are categorized as -constants-; -physical **fault symptoms** -, **grouped** into -physical **fault symptom groups** -, -**symptom** attributes-, -physical **fault syndromes**-, -physical **fault hypotheses**-, and -rules-, and are directly represened by this domain-specific terminology for knowledge classes...in this elementary case are selfexplanatory. The next table 1B first lists the first physical **fault symptom group** GROUPl (there are three physical **fault symptoms**). Each of the physical **fault symptoms** may be prompted either alone or in combination. For one of the three, a **fault symptom** attribute may be added. The final part of the table gives the second (less often...)

...CLAIMS claimed in any of Claims 1 to 5, wherein items are categorized as -constants-; -physical **fault symptoms** -, **grouped** into -physical **fault symptom groups** -, -**symptom** attributes-, -physical **fault syndromes**-, -physical **fault hypotheses**-, and -rules-, and are directly represened by this domain-specific terminology for knowledge classes...

27/5,K/4 (Item 1 from file: 349)

DIALOG(R)File 349:PCT FULLTEXT

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00857190 **Image available**

A NETWORK DEVICE FOR SUPPORTING MULTIPLE UPPER LAYER NETWORK PROTOCOLS OVER
A SINGLE NETWORK CONNECTION

**DISPOSITIF DE RESEAU COMPATIBLE AVEC PLUSIEURS PROTOCOLES DE RESEAU A
COUCHE SUPERIEURE VIA UNE SEULE CONNEXION RESEAU**

Patent Applicant/Assignee:

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US (Residence), US (Nationality)

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PEARSON Terrence S, 8 Hills Farm Lane, Hollis, NH 03049, US,
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POTHIER Peter, 54 Maplewood Drive, Townsend, MA 01469, US,
MANOR Larry B, 15 Cross Road, Londonderry, NH 03053, US,

Legal Representative:

ENGELLENNER Thomas J (et al) (agent), Nutter, McClennen & Fish, LLP, One
International Place, Boston, MA 02110-2699, US,

Patent and Priority Information (Country, Number, Date):

Patent: WO 200190843 A2-A3 20011129 (WO 0190843)

Application: WO 2001US15867 20010516 (PCT/WO US0115867)

Priority Application: US 2000574343 20000520; US 2000574341 20000520; US
2000574440 20000520; US 2000588398 20000606; US 2000591193 20000609; US
2000593034 20000613; US 2000596055 20000616; US 2000613940 20000711; US
2000616477 20000714; US 2000625101 20000724; US 2000633675 20000807; US
2000637800 20000811; US 2000653700 20000831; US 2000656123 20000906; US
2000663947 20000918; US 2000669364 20000926; US 2000687191 20001012; US
2000703856 20001101; US 2000711054 20001109; US 2000718224 20001121; US
2001756936 20010109; US 2001777468 20010205; US 2001789665 20010221; US
2001803783 20010312; US 2001832436 20010410

Designated States:

(Protection type is "patent" unless otherwise stated - for applications
prior to 2004)

AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CO CR CU CZ DE DK DM DZ
EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR LS
LT LU LV MA MD MG MK MN MW MX MZ NO NZ PL PT RO RU SD SE SG SI SK SL TJ
TM TR TT TZ UA UG UZ VN YU ZA ZW
(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE TR
(OA) BF BJ CF CG CI CM GA GN GW ML MR NE SN TD TG
(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZW
(EA) AM AZ BY KG KZ MD RU TJ TM

Main International Patent Class: G06F-013/00

International Patent Class: **G06F-017/30** ; G06F-001/18; **G06F-011/30** ;
G06F-012/14; G06F-003/14; H04L-012/56; H04M-001/10; H04M-007/00;
H04M-003/00; H01J-003/14

Publication Language: English

Filing Language: English

Fulltext Availability:

Detailed Description

Claims

Fulltext Word Count: 210510

English Abstract

The present invention provides a network device with at least one
physical interface or port (44,68) that is capable of transferring
network packets including data organized into one or more upper layer
network protocols. Network packets are received by the port (44,68) and a

port subsystem in accordance with a physical layer network protocol and transferred to forwarding subsystems within the network device in accordance with the upper layer protocols into which the network packets data has been organized. Network packets including data organized in accordance with ATM are then transferred to one or more ATM forwarding subsystems, network packets including data organized in accordance with MPLS are transferred to one or more MPLS forwarding subsystems, and network packets including data organized in accordance with IP are transferred to one or more IP forwarding subsystems.

French Abstract

L'invention concerne un dispositif de reseau comportant au moins une interface ou port physique pouvant transferer des paquets de reseau contenant des donnees organisees en un ou plusieurs protocoles reseau a couche superieure (par exemple, ATM, MPLS, IP, Frame Relay, Voice, Circuit Emulation). Ledit port peut etre connecte a une annexe de reseau afin de permettre que le dispositif de reseau puisse transferer des paquets de reseau avec d'autres dispositifs de reseau. Des paquets de reseau sont recus par le port et un sous-systeme de port conforme a un protocole de reseau a couche physique, puis transferes vers des sous-systemes de reexpedition a l'interieur du dispositif de reseau conformes aux protocoles a couche superieure dans lesquels les donnees de paquets de reseau ont ete organisees. Par exemple, les donnees organisees conformement a ATM via SONET, MPLS via SONET et IP via SONET peuvent etre transferees via une annexe de reseau vers un port du dispositif de reseau. Les paquets de reseau contenant des donnees organisees conformement a ATM sont ensuite transferes vers un ou plusieurs sous-systemes de reexpedition ATM et les paquets de reseau contenant des donnees organisees conformement a IP sont transferes sur un ou plusieurs sous-systemes de reexpedition IP. Pour une efficacite accrue, ce dispositif de reseau permet a l'administrateur de reseau de n'ajouter que le nombre et les types de sous-systemes de reexpedition necessaires pour repondre au service de reseau souscrit pour chaque protocole de reseau a couche. Par ailleurs, ce dispositif de reseau peut necessiter moins d'interfaces physiques que les dispositifs de reseau anterieurs.

Legal Status (Type, Date, Text)

Publication 20011129 A2 Without international search report and to be republished upon receipt of that report.
Search Rpt 20020704 Late publication of international search report
Republication 20020704 A3 With international search report.
Examination 20021205 Request for preliminary examination prior to end of 19th month from priority date

International Patent Class: **G06F-017/30** ...

... **G06F-011/30**

Fulltext Availability:

Detailed Description

Claims

Detailed Description

... FCAPS buttons provide a "status bar code" allowing a network administrator to quickly determine the **category** of **error** or warning and quickly take action to address the **error** or warning.

As with current NMSs, a network administrator may actively monitor the FCAPS buttons...

...device tree 898.

Events tab 957a (Fig. 7E) displays an event number, date, time, source,

category and description of each. **fault** associated ...or port selected in device mimic 896a. System Log tab 957b (Fig. 7F) displays an **event** number, date, time, source, **category** and description of each **fault** associated with the entire network device (e.g., 192 9.201). and Trap Destination tab...a SONET application, the NMS creates an application group table. Referring to Fig. 141), ATM **group** table 108 **indicates** that four instances of ATM (Le., group number 1, 2, 3, 4) - corresponding to !bur...

Claim

... timing reference signals and further comprising: receiving a different one of the first timing reference **signals** and a different one of the second timing reference signals at a second local timing subsystem; selecting one of the different first and second timing reference **signals**; and selecting automatically the other different first and second timing reference **signal** in accordance with a status detected on the selected different first and second timing reference **signal** .
196. A method of operating a network device, comprising:
providing a first source timing **signal** from a first central timing subsystem; providing a first timing reference signal including a first...

?

? t29/5,k/5,7

29/5,K/5 (Item 1 from file: 349)

DIALOG(R) File 349:PCT FULLTEXT

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01219862 **Image available**

DEFECT-TOLERANT AND FAULT-TOLERANT CIRCUIT INTERCONNECTIONS

INTERCONNEXIONS DE CIRCUIT TOLERANT LES DEFAUTS ET TOLERANT LES FAUTES

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Legal Representative:

LEE Denise A (agent), Hewlett-Packard Company, Intellectual Property
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Patent and Priority Information (Country, Number, Date):

Patent: WO 200526957 A2-A3 20050324 (WO 0526957)
Application: WO 2004US29333 20040908 (PCT/WO US04029333)
Priority Application: US 2003659892 20030910

Designated States:

(All protection types applied unless otherwise stated - for applications
2004+)

AE AG AL AM AT AU AZ BA BB BG BR BW BY BZ CA CH CN CO CR CU CZ DE DK DM
DZ EC EE EG ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC
LK LR LS LT LU LV MA MD MG MK MN MW MX MZ NA NI NO NZ OM PG PH PL PT RO
RU SC SD SE SG SK SL SY TJ TM TN TR TT TZ UA UG US UZ VC VN YU ZA ZM ZW
(EP) AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HU IE IT LU MC NL PL PT RO
SE SI SK TR
(OA) BF BJ CF CG CI CM GA GN GQ GW ML MR NE SN TD TG
(AP) BW GH GM KE LS MW MZ NA SD SL SZ TZ UG ZM ZW
(EA) AM AZ BY KG KZ MD RU TJ TM

Main International Patent Class: G11C-008/20

International Patent Class: **G06F-011/10**

Publication Language: English

Filing Language: English

Fulltext Availability:

Detailed Description
Claims

Fulltext Word Count: 18821

English Abstract

Methods for increasing defect tolerance and fault tolerance in systems containing interconnected components, in which a **signal** level is **classified** as belonging to one of a plurality of different, distinguishable classes based on one or more thresholds separating the **signal** -level **classes** , and **defect** -and- **fault** tolerant systems embodying the methods. An electronic-device embodiment including an array of nanowire crossbars, the nanoscale memory elements within the nanowire crossbars addressed through conventional microelectronic address lines, and a method embodiment for providing fault-tolerant interconnection interfaces with electrically distinguishable signal levels are described. In the described embodiment, in order to interconnect microelectronic

address lines with the nanowire crossbars within the electronic memory, an address encoding technique is employed to generate a number of redundant, parity-check address lines to supplement a minimally required set of address signal lines needed to access the nanoscale memory elements.

French Abstract

L'invention porte sur des procedes permettant d'augmenter la tolerance des defauts et la tolerance des fautes des systemes contenant des composants interconnectes, un niveau de signal etant classe comme appartenant a une classe parmi une pluralite de classes differentes pouvant etre distinguees, selon un ou plusieurs seuils qui separent les classes de niveaux de signaux, ainsi que sur des systemes de tolerance des defauts et des fautes qui renferment ces procedes. L'invention concerne aussi un mode de realisation d'un dispositif electronique comprenant un reseau de barres transversales de nanofils, des elements de memoire nanometriques contenus dans les barres transversales de nanofils etant envoyes au moyen de lignes d'adresse micro-electronique habituelles, ainsi qu'un mode de realisation d'un procede permettant de fournir des interfaces d'interconnexion tolerant les fautes avec des niveaux de signaux pouvant etre electriquement distingues. Dans le mode de realisation decrit, afin d'interconnecter les lignes d'adresse micro-electronique avec les barres transversales de nanofils dans la memoire electronique, une technique de codage d'adresse permet de generer un certain nombre de lignes d'adresse de controle de parite excedentaires afin de completer un ensemble minimum indispensable de lignes de signaux d'adresse necessaires pour acceder aux elements de memoire nanometriques.

Legal Status (Type, Date, Text)

Publication 20050324 A2 Without international search report and to be republished upon receipt of that report.

Search Rpt 20050623 Late publication of international search report

Republication 20050623 A3 With international search report.

Republication 20050623 A3 Before the expiration of the time limit for amending the claims and to be republished in the event of the receipt of amendments.

International Patent Class: G06F-011/10

Fulltext Availability:

Detailed Description

English Abstract

...for increasing defect tolerance and fault tolerance in systems containing interconnected components, in which a **signal** level is **classified** as belonging to one of a plurality of different, distinguishable classes based on one or more thresholds separating the **signal** -level **classes** , and **defect** -and- **fault** tolerant systems embodying the methods. An electronic-device embodiment including an array of nanowire crossbars...

Detailed Description

... general, to fabricate the nanoscale interconnection interface so that failures resulting in permanent open crosspoints **occur** with much greater **frequency** than failures that result in a permanent short at a crosspoint. Thus, in the present...

29/5,K/7 (Item 3 from file: 349)

DIALOG(R) File 349:PCT FULLTEXT

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00171884

METHODS AND APPARATUS FOR MONITORING AND DIAGNOSING SYSTEM PERFORMANCE
PROCEDES ET APPAREIL DE CONTROLE ET DE DIAGNOSTIC DU FONCTIONNEMENT D'UN
SYSTEME

Patent Applicant/Assignee:

ALLIED-SIGNAL INC,

Inventor(s):

McCOWN Patricia Millington,

CONWAY Timothy James,

JESSEN Karl Michael,

Patent and Priority Information (Country, Number, Date):

Patent: WO 9005337 A2 19900517

Application: WO 89US4709 19891102 (PCT/WO US8904709)

Priority Application: US 88722 19881103; US 89464 19890410

Designated States:

(Protection type is "patent" unless otherwise stated - for applications prior to 2004)

AT BE CH DE FR GB IT JP LU NL SE

Main International Patent Class: **G06F-011/22**

Publication Language: English

Fulltext Availability:

Detailed Description

Claims

Fulltext Word Count: 15223

English Abstract

Method and apparatus for performing system monitoring and diagnostics is disclosed. In performing system monitoring, data is acquired from the system under test and compared to an event model. The event model comprises a database having event records which pre-define events which can occur the system's performance. Each event record includes a state vector dependency which lists the events which must occur prior to the pre-defined event occurring and one or more critical parameters defining the data which must occur during the system's performance for the event to have occurred. Event recognition is performed by comparing each event record to acquired operational data and to events already recognized. Associated with each event record in the database is an intelligent data acquisition action which defines an action to be taken as a result of the event record being recognized. These actions can modify the performance of the system being monitored or the acquisition of data. Additionally, the results of the event recognition step can be further analyzed in a computer.

French Abstract

Procede et appareil permettant de controler et de diagnostiquer le fonctionnement d'un systeme, consistant a acquerir des donnees du systeme en cours d'essai et a les comparer a un modele d'evenements. Le modele d'evenements comprend une base de donnees ayant des enregistrements d'evenements qui definissent a l'avance des evenements pouvant se produire dans le fonctionnement du systeme. Chaque enregistrement d'evenements comprend une dependance vectorielle d'etat qui etablit une liste des evenements qui doivent avoir lieu avant l'evenement pre-defini et un ou plusieurs parametres critiques definissant les donnees qui doivent se produire pendant le fonctionnement du systeme pour l'evenement ayant eu lieu. La reconnaissance des evenements est effectuee en comparant chaque enregistrement d'evenements avec les donnees acquises de fonctionnement et avec des evenements deja reconnus. A chaque enregistrement d'evenements dans la base de donnees est associee une action d'acquisition de donnees intelligentes qui definit une action ou decision a prendre suite a la reconnaissance de l'enregistrement d'evenements. Ces actions peuvent modifier le fonctionnement du systeme controle ou l'acquisition des donnees. De plus, les resultats de la

reconnaissance d'evenements peuvent etre analyses dans un ordinateur.

Main International Patent Class: **G06F-011/22**

Fulltext Availability:

Detailed Description

Detailed Description

... under test are of low quality or if
any step yields consistently poor results they **occur** more
frequently in the case of heuristic rule based knowledge
representations, wherein an adequate set of rules ...describes whether
the
component is part of another group of components or consists
of a **group** of components. The performance **characteristics**
of the component are also included in its dynamic
characteristics ,
To use the **Event** Structured Component Model 410, the
5 operational history of the APU contained in the state...

?

? t44/5,k/2-5,8

44/5,K/2 (Item 2 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
(c) 2005 European Patent Office. All rts. reserv.

00903888

Error indication for a storage system with removable media
Fehleranzeige fur ein Speichersystem mit auswechselbaren Speichereinheiten
Indication d'erreurs pour un systeme de stockage avec des unites de
stockage exchangeables

PATENT ASSIGNEE:

Hewlett-Packard Company, (206030), 3000 Hanover Street, Palo Alto,
California 94304, (US), (applicant designated states: DE;ES;FR;GB)

INVENTOR:

Gold, Stephen, Rock Cottage, 80 Stone Lane, Winterbourne Down,, Bristol
BS17 1DJ, (GB)
Lord, Jonathan Conrad, 66 Kings Drive, Bishopston,, Bristol BS7 8JH, (GB)
Turner, Philip John, 63 Nightingale Rise, Portishead, Bristol BS20 8LN,
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LEGAL REPRESENTATIVE:

Lawman, Matthew John Mitchell (84551), Hewlett-Packard Limited, IP
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PATENT (CC, No, Kind, Date): EP 825537 A1 980225 (Basic)
APPLICATION (CC, No, Date): EP 97305464 970722;
PRIORITY (CC, No, Date): EP 96305391 960723
DESIGNATED STATES: DE; ES; FR; GB
INTERNATIONAL PATENT CLASS: G06F-011/32

ABSTRACT EP 825537 A1

The application relates to storage systems for computer data and
relates particularly to diagnosis of errors and monitoring of such a
system.

The storage system comprises a controller and a storage device that can
read data from and write data to removable media, like for example a tape
drive.

The application provides a system for storing computer data in which
errors are categorised according to severity, and in which low-level
errors can trigger a combination of high level error conditions.

These high level error conditions are then communicated to the
controller in order to provide error messages to the user.

ABSTRACT WORD COUNT: 101

LEGAL STATUS (Type, Pub Date, Kind, Text):

Examination: 001129 A1 Date of dispatch of the first examination
report: 20001013
Application: 980225 A1 Published application (A1with Search Report
;A2without Search Report)
Withdrawal: 020109 A1 Date of withdrawal of application: 20011112
Assignee: 010328 A1 Transfer of rights to new applicant:
Hewlett-Packard Company, A Delaware Corporation
(3016020) 3000 Hanover Street Palo Alto, CA
94304 US
Change: 980318 A1 Inventor (change)
Examination: 980923 A1 Date of filing of request for examination:
980723
Change: 981104 A1 Designated Contracting States (change)
Change: 990120 A1 Designated Contracting States (change)
LANGUAGE (Publication,Procedural,Application): English; English; English
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	9809	615
SPEC A	(English)	9809	4632
Total word count - document A			5247
Total word count - document B			0
Total word count - documents A + B			5247

INTERNATIONAL PATENT CLASS: **G06F-011/32**

...SPECIFICATION feature may help the user in prioritising the necessary error recovery steps.

The low-level **error indicators** may be **grouped** into functional types and the high level error conditions may be communicated according to what functional type of low-level **error** has **occurred**. This feature may facilitate specifying the relationship between low-level error indicators and the high...

...CLAIMS pertaining error conditions.

4. A system according to any preceding claim wherein the low-level **error indicators** are **grouped** into functional types and wherein high level error conditions are communicated according to what functional type of low-level **error** has **occurred**.
5. A system according to any preceding claim comprising means automatically to initiate a predetermined...

44/5,K/3 (Item 3 from file: 348)

DIALOG(R)File 348:EUROPEAN PATENTS

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00899003

Error indication for a storage system with removable media

Fehleranzeige für ein Speichersystem mit auswechselbaren Speichereinheiten

Indication d'erreurs pour un système de stockage avec des unités de stockage échangeables

PATENT ASSIGNEE:

Hewlett-Packard Company, (206030), 3000 Hanover Street, Palo Alto, California 94304, (US),

INVENTOR:

Gold, Stephen, Rock Cottage, 80 Stone Lane, Winterbourne Down, Bristol BS17 1DJ, (GB)

Lord, Jonathan Conrad, 66 Kings Drive, Bishipston, Bristol BS7 8JH, (GB)

Turner, Philip John, 63 Nightingale Rise, Portishead, Bristol BS20 8LN, (GB)

LEGAL REPRESENTATIVE:

Kilgannon, Denise Mary et al (53151), Hewlett-Packard Ltd, IP Section, Building 2, Filton Road, Stoke Gifford, Bristol BS12 6QZ, (GB)

PATENT (CC, No, Kind, Date): EP 821308 A1 980128 (Basic)

APPLICATION (CC, No, Date): EP 96305391 960723;

PRIORITY (CC, No, Date): EP 96305391 960723

DESIGNATED STATES: AT; BE; CH; DE; DK; ES; FI; FR; GB; GR; IE; IT; LI; LU; MC; NL; PT; SE

INTERNATIONAL PATENT CLASS: **G06F-011/32**

ABSTRACT EP 821308 A1

The application relates to storage systems for computer data and relates particularly to diagnosis of errors and monitoring of such a system.

The storage system comprises a controller and a storage device that can read data from and write data to removable media, like for example a tape

drive.

The application provides a system for storing computer data in which errors are categorised according to severity, and in which low-level errors can trigger a combination of high level error conditions.

These high level error conditions are then communicated to the controller in order to provide error messages to the user.

ABSTRACT WORD COUNT: 101

LEGAL STATUS (Type, Pub Date, Kind, Text):

Application: 980128 A1 Published application (A1with Search Report
;A2without Search Report)

Change: 981007 A1 Designated Contracting States (change)

Change: 981209 A1 Designated Contracting States (change)

Withdrawal: 990512 A1 Date on which the European patent application
was deemed to be withdrawn: 980729

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	9805	387
SPEC A	(English)	9805	3777
Total word count - document A			4164
Total word count - document B			0
Total word count - documents A + B			4164

INTERNATIONAL PATENT CLASS: G06F-011/32

...SPECIFICATION feature may help the user in prioritising the necessary error recovery steps.

The low-level **error indicators** may be **grouped** into functional types and the high level error conditions may be communicated according to what functional type of low-level **error** has **occurred**. This feature may facilitate specifying the relationship between low-level error indicators and the high...

...CLAIMS pertaining error conditions.

4. A system according to any preceding claim wherein the low-level **error indicators** are **grouped** into functional types and wherein high level error conditions are communicated according to what functional type of low-level **error** has **occurred**.
5. A system according to any preceding claim comprising means automatically to initiate a predetermined...

44/5,K/4 (Item 4 from file: 348)

DIALOG(R)File 348:EUROPEAN PATENTS

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00899002

Error indication for a storage system with removable media

Fehleranzeige für ein Speichersystem mit auswechselbaren Speichereinheiten

Indication d'erreurs pour un système de stockage avec des unités de
stockage échangeables

PATENT ASSIGNEE:

Hewlett-Packard Company, A Delaware Corporation, (3016020), 3000 Hanover
Street, Palo Alto, CA 94304, (US), (Proprietor designated states: all)

INVENTOR:

Gold, Stephen, Rock Cottage, 80 Stone Lane, Winterbourne Down, Bristol
BS17 1DJ, (GB)

Lord, Jonathan Conrad, 66 Kings Drive, Bishopston, Bristol BS7 8JH, (GB)

Turner, Philip John, 63 Nightingale Rise, Portishead, Bristol BS20 8LN,

(GB)

LEGAL REPRESENTATIVE:

Kilgannon, Denise Mary et al (53151), Hewlett-Packard Ltd, IP Section,
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PATENT (CC, No, Kind, Date): EP 821307 A1 980128 (Basic)
EP 821307 B1 030502

APPLICATION (CC, No, Date): EP 96305388 960723;

PRIORITY (CC, No, Date): EP 96305388 960723

DESIGNATED STATES: DE; FR; GB

INTERNATIONAL PATENT CLASS: G06F-011/32 ; G06F-011/22

CITED PATENTS (EP B): US 4549295 A; US 5450609 A

CITED REFERENCES (EP B):

IBM TECHNICAL DISCLOSURE BULLETIN, vol. 37, no. 2B, 1 February 1994, page
241/242 XP000433828 "KEEPING USERS AWARE OF THE STATUS OF AN OBJECT
AFFECTED BY MULTIPLE PROGRAMS"

IBM TECHNICAL DISCLOSURE BULLETIN, vol. 37, no. 6B, 1 June 1994, pages
499-501, XP000456077 "OPERATOR INTERVENTION MESSAGE MANAGEMENT FOR TAPE
DRIVE DISPLAYS";

ABSTRACT EP 821307 A1

The application relates to storage systems for computer data and
relates particularly to diagnosis of errors and status monitoring of such
a system.

The storage system comprises a controller and a storage device that can
read data from and write data to removable media, like for example a tape
drive.

The application provides a system for storing computer data in which
different error messages are generated according to how many times a
specified low-level error has occurred.

In particular embodiment, different combinations of error messages are
generated according to how many times a particular low-level error has
occurred.

ABSTRACT WORD COUNT: 99

NOTE:

Figure number on first page: 1

LEGAL STATUS (Type, Pub Date, Kind, Text):

Examination: 001129 A1 Date of dispatch of the first examination
report: 20001013

Application: 980128 A1 Published application (A1with Search Report
;A2without Search Report)

Oppn None: 040421 B1 No opposition filed: 20040203

Assignee: 010328 A1 Transfer of rights to new applicant:
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(3016020) 3000 Hanover Street Palo Alto, CA
94304 US

Grant: 030502 B1 Granted patent

Examination: 980923 A1 Date of filing of request for examination:
980723

Change: 981007 A1 Designated Contracting States (change)

Change: 990210 A1 Designated Contracting States (change)

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	199805	353
CLAIMS B	(English)	200318	473
CLAIMS B	(German)	200318	459
CLAIMS B	(French)	200318	540
SPEC A	(English)	199805	6721
SPEC B	(English)	200318	6581
Total word count - document A			7075

Total word count - document B 8053
Total word count - documents A + B 15128

INTERNATIONAL PATENT CLASS: G06F-011/32 ...

... G06F-011/22

...SPECIFICATION combinations of high level error conditions according to how many times a particular low-level **error** has **occurred** .

Preferably, the low-level **error indicators** are **grouped** into functional types and a predetermined sequence of high level error conditions is communicated according...

...CLAIMS combinations of high level error conditions according to how many times a particular low-level **error** has **occurred** .

3. A system according to any preceding claim wherein the low-level **error indicators** are **grouped** into functional types and wherein a predetermined sequence of high level error conditions is communicated ...

...CLAIMS combination of error condition flags to the controller according to how many times a specified **error indicator** has **occurred** .

2. A storage device according to any preceding claim wherein the **error indicators** are **grouped** into functional types and wherein a predetermined sequence of error condition flags is communicable to...

44/5,K/5 (Item 5 from file: 348)

DIALOG(R)File 348:EUROPEAN PATENTS

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00888170

PROCESS CONTROL SYSTEM

PROZESSSTEUERUNGSSYSTEM

SYSTEME DE COMMANDE DE PROCESSUS

PATENT ASSIGNEE:

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INVENTOR:

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OHYAMA, Yuichi, 652-24, Naganuma-machi, Isezaki-shi, Gunma 372, (JP)

SUGIMOTO, Hidekuni, 15-1, Midori 2-chome, Honjyo-shi, Saitama 367, (JP)

ISHIKAWA, Seiji, 420-504, Noboritoshinmachi, Tama-ku, Kawasaki-shi, Kanagawa 214, (JP)

SHIBA, Masataka, 2-1-5-602, Ryokuen 4-chome, Izumi-ku, Yokohama-shi, Kanagawa 245, (JP)

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YOKOUCHI, Tetsuji, Mitake-ryo, 1545, Yoshida-cho, Totsuka-ku,
 Yokohama-shi, Kanagawa 244, (JP)
 HAMADA, Toshimitsu, 66-14, Torigaoka, Totsuka-ku, Yokohama-shi, Kanagawa
 244, (JP)
 SUZUKI, Ikuo, 1235-2, Ichige, Hitachinaka-shi, Ibaraki 312, (JP)
 IKOTA, Masami, 1881-10, Mukouhara 1-chome, Higashiyamato-shi, Tokyo 207,
 (JP)
 NOZOE, Mari, Kurefoto kabe 103, 2-27, Kabemachi 7-chome, Oume-shi, Tokyo
 198, (JP)
 MIYAZAKI, Isao, 1331-3, Higashikaminomiyamachi, Isesaki-shi, Gunma 372,
 (JP)
 SHIGYO, Yoshiharu, 193-4, Namiemachi, Takasaki-shi, Gunma 370, (JP)
 LEGAL REPRESENTATIVE:
 Strehl Schubel-Hopf & Partner (100941), Maximilianstrasse 54, 80538
 Munchen, (DE)
 PATENT (CC, No, Kind, Date): EP 910123 A1 990421 (Basic)
 WO 9735337 970925
 APPLICATION (CC, No, Date): EP 97907383 970319; WO 97JP898 970319
 PRIORITY (CC, No, Date): JP 9663014 960319; JP 9663013 960319; JP 9663012
 960319
 DESIGNATED STATES: DE; GB
 INTERNATIONAL PATENT CLASS: H01L-021/66; G01B-011/30; G01N-021/88;
G06F-015/00 ; H04K-001/00; G09C-001/00

ABSTRACT EP 910123 A1

A process management system in accordance with the present invention comprising inspection apparatuses for inspecting defects on a wafer, the inspection apparatuses being connected through a communication network, inspection information and image information obtained from these inspection apparatuses being collected to construct a data base and an image file, therein definition of defects is given by combinations of elements which characterize the defect based on the inspection information and the image information obtained from the inspection apparatuses.

By giving definition of the defect, characteristics of the defect can be subdivided and known. Therefore, it is easy to study what cause a defect is produced by.

ABSTRACT WORD COUNT: 106

LEGAL STATUS (Type, Pub Date, Kind, Text):

Search Report: 000823 A1 Date of drawing up and dispatch of
 supplementary:search report 20000706
 Application: 971210 A1 International application (Art. 158(1))
 Examination: 040714 A1 Date of dispatch of the first examination
 report: 20040211
 Application: 990421 A1 Published application (A1with Search Report
 ;A2without Search Report)
 Examination: 990421 A1 Date of filing of request for examination:
 981015

LANGUAGE (Publication,Procedural,Application): English; English; Japanese
 FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	9916	1524
SPEC A	(English)	9916	15815
Total word count - document A			17339
Total word count - document B			0
Total word count - documents A + B			17339

...INTERNATIONAL PATENT CLASS: **G06F-015/00**

...SPECIFICATION a defect image.

2. Automatically classifying by a computer.

In the above item 1, the **defect** image and the **classification** code - **characteristic** item corresponding table shown in FIG. 10 are displayed on the display unit of, for...

...of the classification result and by making the computer learn the correct result.

Practically, a **defect classification** table is formed using **characteristic** items extracted from the image information such as shape of defect, size, color, composition, structure...

...extracted from the inspection information such as defect producing process, defect distribution on wafer surface, **defect occurrence** position and the like, and defect classification is performed by checking an extracted **characteristic** of the detected **defect** with the defect classification table.

Particularly, by **classifying defect** shapes not into geometrical **characteristics** but into types based on short circuit, line break, projection, chipping-off, pin hole, isolation...

44/5,K/8 (Item 8 from file: 348)

DIALOG(R)File 348:EUROPEAN PATENTS

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00330519

SYSTEM FOR MONITORING AND ANALYSIS OF A CONTINUOUS PROCESS.

SYSTEM ZUR UBERWACHUNG UND ANALYSE EINES KONTINUIERLICHEN PROZESSES.

SYSTEME DE SURVEILLANCE ET D'ANALYSE DE PROCESSUS CONTINUS.

PATENT ASSIGNEE:

EASTMAN KODAK COMPANY (a New Jersey corporation), (201210), 343 State Street, Rochester New York 14650, (US), (applicant designated states: DE;FR;GB)

INVENTOR:

ZOELLER, Leon, R., 3652 Brick School House Road, Hamlin, NY 14464, (US)
BUTTON, Roger, E., 1655 Creek Street, Rochester, NY 14625, (US)
GABELLO, Louis, R., 251 Bellehurst Drive, Rochester, NY 14617, (US)
DI VINCENZO, Joseph, P., 263 Dohrcrest Drive, Rochester, NY 14612, (US)
LANGE, Thomas, O., 297 Pearson Lane, Rochester, NY 14612, (US)

LEGAL REPRESENTATIVE:

Buff, Michel et al (14411), Kodak-Pathe Departement des Brevets et Licences CRT Centre de Recherches et de Technologie Zone Industrielle, F-71102 Chalon sur Saone Cedex, (FR)

PATENT (CC, No, Kind, Date): EP 312580 A1 890426 (Basic)
EP 312580 B1 931103
WO 8808588 881103

APPLICATION (CC, No, Date): EP 88904330 880429; WO 88US1373 880429

PRIORITY (CC, No, Date): US 45357 870501

DESIGNATED STATES: DE; FR; GB

INTERNATIONAL PATENT CLASS: **G06F-015/70 ; G06F-015/74**

CITED PATENTS (WO A): EP 93422 A; GB 2129545 A; EP 18861 A; US 4237539 A

CITED REFERENCES (EP A):

See also references of WO8808588;

CITED REFERENCES (WO A):

Machine Design, volume 58, no. 2, January 1986, (Cleveland, Ohio, US) J.

Sylvan: "Multiprocessing for industrial control", pages 67-71;

NOTE:

No A-document published by EPO

LEGAL STATUS (Type, Pub Date, Kind, Text):

Application: 890426 A1 Published application (A1with Search Report
;A2without Search Report)

Examination: 890426 A1 Date of filing of request for examination:
890106
Examination: 910605 A1 Date of despatch of first examination report:
910423
Grant: 931103 B1 Granted patent
Oppn None: 941026 B1 No opposition filed
Lapse: 970423 B1 Date of lapse of the European patent in a
Contracting State: GB 960429

LANGUAGE (Publication,Procedural,Application): English; English; English
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	EPBBF1	555
CLAIMS B	(German)	EPBBF1	565
CLAIMS B	(French)	EPBBF1	627
SPEC B	(English)	EPBBF1	6725
Total word count - document A			0
Total word count - document B			8472
Total word count - documents A + B			8472

INTERNATIONAL PATENT CLASS: G06F-015/70 ...

... G06F-015/74

...SPECIFICATION EP-A-093,422 discloses a method and apparatus for real time automatic detection and **classification** of **characteristic** type surface **imperfections** **occurring** on the surfaces of a moving material.
The apparatus comprises a data camera which transversely...

?

52/5,K/1 (Item 1 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
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01278541

**A method and kit for assessing and improving internationalization readiness
of a software product**

**Verfahren und Vorrichtung zum Einschätzen und Verbesserung von der
Internationalisierungsfähigkeit eines Softwareproduktes**

**Procede et dispositif d'evaluation et amelioration de
l'internationalisation d'un produit logiciel**

PATENT ASSIGNEE:

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PATENT (CC, No, Kind, Date): EP 1100012 A2 010516 (Basic)
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APPLICATION (CC, No, Date): EP 2000309639 001101;

PRIORITY (CC, No, Date): US 432727 991102

DESIGNATED STATES: DE; GB

EXTENDED DESIGNATED STATES: AL; LT; LV; MK; RO; SI

INTERNATIONAL PATENT CLASS: G06F-009/44

ABSTRACT EP 1100012 A2

A method and kit for assessing and improving internationalization readiness of a predetermined software product selected for evaluation by a user is provided. The method allows for generating an electronic master scorecard listing data **indicative** of respective main **categories** of internationalization topics to be considered by the user. The master scorecard may list respective counts **indicative** of each opportunity that a **defect** may **occur** in the software product. The master scorecard may further list respective counts indicative of actual defects relative to each respective one of the listed main categories topics. A computing step allows for computing a statistical measurement indicative of the readiness of the software product relative to the main categories of topics listed in the master scorecard.

ABSTRACT WORD COUNT: 121

NOTE:

Figure number on first page: NONE

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Examination: 040915 A2 Date of request for examination: 20040721
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report: 20050211

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FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	200120	582
SPEC A	(English)	200120	6534
Total word count - document A			7116
Total word count - document B			0
Total word count - documents A + B			7116

...ABSTRACT a user is provided. The method allows for generating an electronic master scorecard listing data **indicative** of respective main **categories** of internationalization topics to be considered by the user. The master scorecard may list respective counts **indicative** of each opportunity that a **defect** may **occur** in the software product. The master scorecard may further list respective counts indicative of actual ...

69/5,K/9 (Item 8 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT
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01168696 **Image available**

METHOD AND APPARATUS FOR SYSTEM MANAGEMENT USING CODEBOOK CORRELATION WITH SYMPTOM EXCLUSION

PROCEDE ET APPAREIL DE GESTION DE SYSTEME A PARTIR D'UNE MISE EN CORRELATION PAR LIVRE DE CODES AVEC EXCLUSION DE SYMPTOMES

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AE AG AL AM AT AU AZ BA BB BG BR BW BY BZ CA CH CN CO CR CU CZ DE DK DM
DZ EC EE EG ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC
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(AP) BW GH GM KE LS MW MZ SD SL SZ TZ UG ZM ZW
(EA) AM AZ BY KG KZ MD RU TJ TM

Main International Patent Class: G06F

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Detailed Description

Claims

Fulltext Word Count: 4335

English Abstract

A method and apparatus are provided for correlating events in a system. Problems and other events can, e.g., be detected in a system that generates **symptoms** or observable **events**. A computer-accessible codebook is provided that includes a mapping between each of a plurality of **groups** of possible **symptoms** and one of a plurality of likely exceptional events (e.g., problems) in the system. The system is monitored and one or more known symptoms generated by the system are detected. A mismatch measure is determined between each of the plurality of groups of possible symptoms in the mapping and the one or more known symptoms using a computer, while disregarding symptoms in the groups of

possible symptoms not determined to be known. One or more of the plurality of likely problems is selected corresponding to one of the plurality of groups having the smallest mismatch measure.

French Abstract

L'invention concerne un procede et un appareil permettant la mise en correlation d'evenements dans un systeme. Des problemes et d'autres evenements peuvent, p. ex., etre detectes dans un systeme generant des symptomes ou des evenements observables. Un livre de codes accessible par ordinateur comporte une table de correspondances entre chacun des groupes de symptomes possibles et un des evenements exceptionnels probables (p. ex. des problemes) dans le systeme. Ce systeme est surveille et un ou plusieurs symptomes connus, generes par ce dernier, sont detectes. Une mesure de non-concordance est determinee entre chacun des groupes de symptomes possibles dans la table de correspondances et le ou les symptomes connus au moyen d'un ordinateur, tandis que des symptomes dans les groupes de symptomes possibles non determines comme connus sont ecartes. Un ou plusieurs des problemes probables correspondant a un des groupes presentant la plus petite mesure de non-concordance sont selectionnes.

Legal Status (Type, Date, Text)

Publication 20041021 A2 Without international search report and to be republished upon receipt of that report.

English Abstract

...system. Problems and other events can, e.g., be detected in a system that generates **symptoms** or observable **events**. A computer-accessible codebook is provided that includes a mapping between each of a plurality of **groups** of possible **symptoms** and one of a plurality of likely exceptional events (e.g., problems) in the system...